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7.29.03

C Moore

N THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicants:

T. SASAKURA, et al.

Application No.:

10/077,779

Filed:

February 20, 2002

For:

SEMICONDUCTOR INTEGRATED CIRCUIT MODULES, MANUFACTURING METHODS AND USAGE THEREOF

Art Unit:

2814

Examiner:

D. P. Nguyen

AMENDMENT

Mail Stop Non Fee Amendment Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450 July 21, 2003

Sir:

In response to the Office Action dated June 26, 2003, please amend the above-identified application as follows:

IN THE CLAIMS:

Please **carcel** claims 9-16 without prejudice or disclaimer of the subject matter contained therein.

JUL 22 2003